

Abstracts

A Compact 1.7--2.1 GHz Three-Way Power Combiner Using Microstrip Technology with Better Than 93.8% Combining Efficiency

D. Maurin and K. Wu. "A Compact 1.7--2.1 GHz Three-Way Power Combiner Using Microstrip Technology with Better Than 93.8% Combining Efficiency." 1996 Microwave and Guided Wave Letters 6.2 (Feb. 1996 [MGWL]): 106-108.

This letter presents the design and measured performance of a microstrip three-way power combiner. The combiner is designed using the conventional Wilkinson topology with the extension to three outputs, which has been rarely considered for the design and fabrication of V-way combiners. It is shown that with an appropriate design approach, the main drawback reported with this topology (nonplanarity of the circuit when $N > 2$) can be minimized to have a negligible effect on the circuit performance and still allow an easy MIC or MHMIC fabrication.

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